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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Jeoung-Mo KOO et al. EXAMINER: Jose R. Diaz  
SERIAL NO.: 10/799,201 GROUP ART UNIT: 2815  
FILED: March 12, 2004  
FOR: PROGRAMMABLE MEMORY DEVICE, INTEGRATED  
CIRCUIT INCLUDING THE PROGRAMMABLE MEMORY  
DEVICE, AND METHOD OF FABRICATING SAME

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

This paper is submitted in response to the December 28, 2005 Office Action issued by the United States Patent and Trademark Office in connection with the above-identified application. A response to the December 28, 2005 Office Action is due on January 28, 2006. Therefore, this paper is being timely filed.

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**CERTIFICATE OF MAILING 37 C.F.R. § 1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 20, 2006.

Dated: January 20, 2006

  
Michael F. Morano